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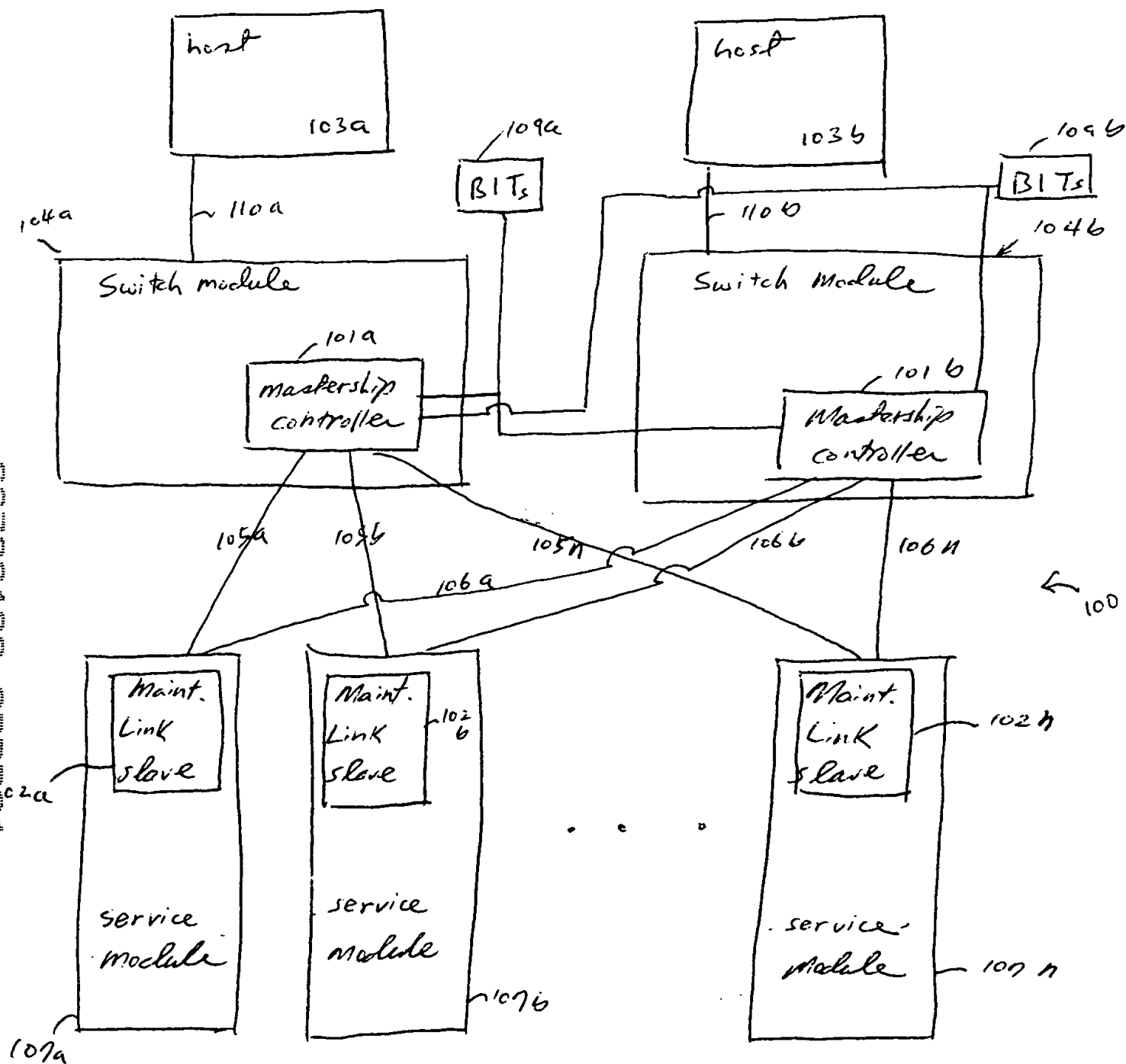


Figure 1

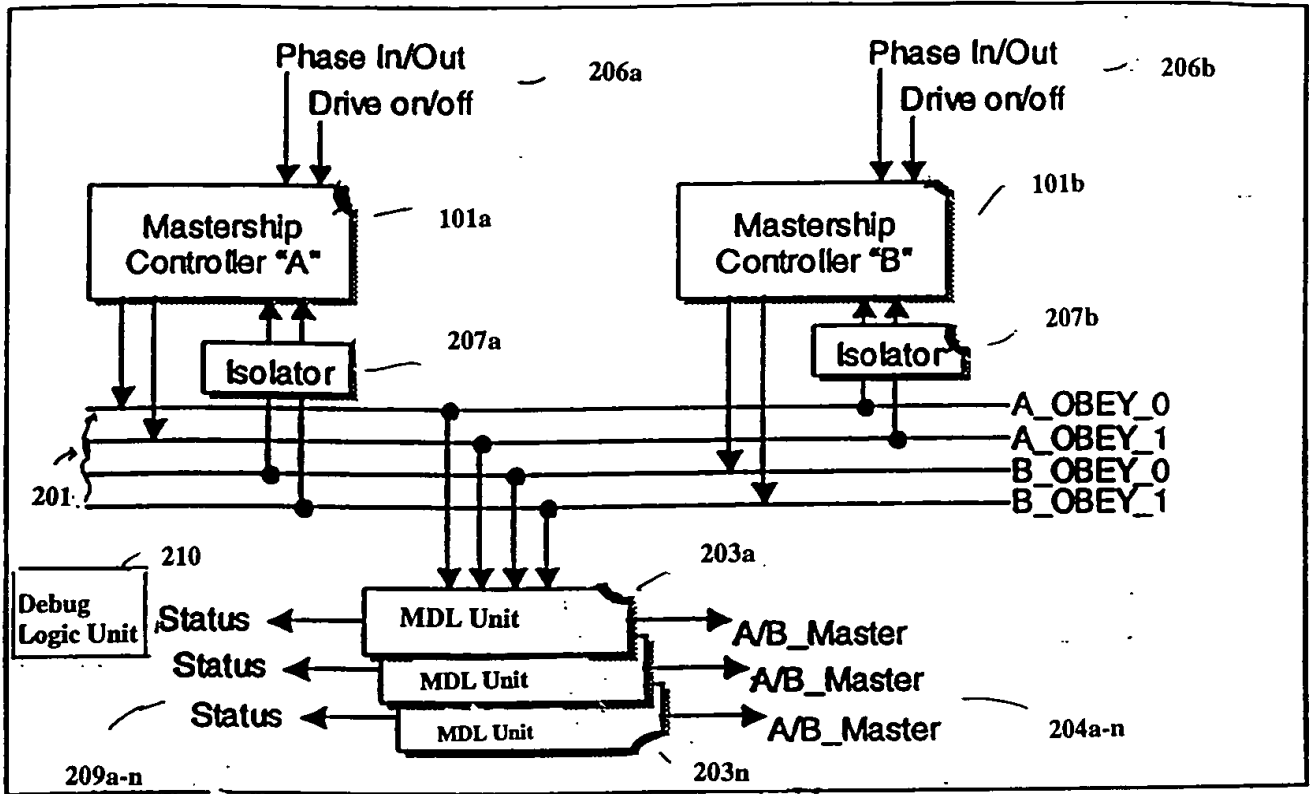


Figure 2: Mastership circuitry

A-OBEY	B-OBEY	Phase	<i>determined</i> Controller to be Working	Comments
None	None	N/A	A	Abnormal condition, included for completeness: normally one controller should want to be master
Clocking	None	N/A	A	Controller_B absent, faulted, or doesn't want to be master
None	Clocking	N/A	B	As above, but for Controller_A
Clocking	Clocking	IN	A	Both controllers are functioning correctly, and A is as the master controller <i>determined</i>
Clocking	Clocking	OUT	B	Both controllers are functioning correctly, and B is as the master controller <i>determined</i>

Figure 3: Mastership *determination* rules with clocked mastership lines